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# SPECIFICATIONS

## GVTQ35SPAK1RS

TFT 3.5" 320x240 Transmissive with T/P  
LED Backlight 250 cd/m2 16 Bit i80 Interface

Item # 108460

Version January 2013



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### 1 General Description and Features

GVTQ35SPAK1RS is a TM (Transmissive) type color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT-LCD module, a driver circuit and a back-light unit and touch screen panel (TSP). The resolution of a 3.5" contains 320RGBx240 dots and can display up to 65K/262K colors. The following table described the features of GVTQ35SPAK1RS.

#### 1.1 Features

- Transmissive and back-light with 6 LEDs are available.
- TN (Twisted Nematic) mode.
- 16 Bits i80 system interface
- RoHS Compliance

#### 1.2 LCD Module

Item	Specification	Unit
Screen Size	3.5 inches	Diagonal
Display Resolution	320(H) x RGB x 240(V)	Dot
Pixel Pixels	0.073 (H) x 0.219 (V)	mm
Active Area	70.08 (H) x 52.56 (V)	mm
Outline Dimension	77.0 (W) x 64.0 (H) x 4.4 (D)	mm
Display Mode	Normally white/Transmissive	--
Pixel Arrangement	RGB Side-Stripe	--
Surface Treatment	Anti-glare (AG)	--
Display Color	65K/262K	--
Viewing Direction	6 o'clock (Gray Inversion)	--
TFT Driver	SSD2119M1Z	--
Input Interface	16 Bits i80 system interface.	--

### 2 Mechanical Information

Item	Min.	Typ.	Max.	Unit	Note	
Module Size	Horizontal (H)	76.7	77.0	77.3	mm	--
	Vertical (V)	63.7	64.0	64.3	mm	(1)
	Thickness (T)	3.15	4.4	3.45	mm	(1)
Weight	--	(60)	--	g	--	

Note (1) Not include FPC.

Refer to the Dimensional Outlines for further information.

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### 3 Electrical Specifications

#### 3.1 Absolute Max. Ratings

##### 3.1.1 Absolute Ratings of Environment

If the operating condition exceeds the following absolute maximum ratings, the TFT LCD module may be damaged permanently.

(Ta=25±2°C, V<sub>SS</sub>=GND=0)

Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	T <sub>STG</sub>	-20	70	°C	(1)
Operating temperature	T <sub>OPR</sub>	-10	60	°C	(1,2,3)

Note (1) 95 % RH Max. ( 40 °C ≥ Ta ). Maximum wet-bulb temperature at 39 °C or less. (Ta > 40 °C)  
No condensation.

Note (2) In case of below 0°, the response time of liquid crystal (LC) becomes slower and the color of panel becomes darker than normal one. Level of retardation depends on temperature, because of LC's character

Note (3) Only operation is guaranteed at operating temperature. Contrast, response time, another display quality are evaluated at +25°C.

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### 3.2 Electrical Absolute Rating

#### 3.2.1 TFT-LCD Module

(Ta=25±2°C, V<sub>SS</sub>=GND=0)

Item	Symbol	Value		Unit	Condition
		Min.	Max.		
Logic power supply	V <sub>DDIO</sub>	-0.3	4.0	V	--
Logic input voltage	V <sub>CI</sub>	V <sub>SS</sub> -0.3	5.0	V	--
Power Supply Current (I <sub>vdd</sub> +I <sub>vci</sub> )	I	--	25	mA	--

Note : Temp. ≤ 60°C, 90% RH MAX.

Temp. > 60°C , absolute humidity shall be less than 90% RH at 60°C

#### 3.2.2 Back-Light Unit

(Ta=25±2°C)

Item	Symbol	Min.	Max.	Unit	Note
Current	I <sub>f</sub>	--	30	mA	(1)

Note (1) Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded. Functional operation should be restricted to the conditions described under normal operating conditions.

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### 4 Electrical Characteristics

#### 4.1 TFT-LCD Module

(Ta=25±2°C, V<sub>DDIO</sub> =3.3V)

Item	Symbol	Value			Unit	Condition
		Min.	Typ.	Max.		
Power supply of IO pins	V <sub>DDIO</sub>	1.4	--	3.6	V	
Booster Reference supply VoltageRange	V <sub>CI</sub>	2.5 or V <sub>DDIO</sub>	--	3.6	V	
Power Supply Current	I <sub>vdd</sub>	-	150	300	μA	
Power Supply Current	I <sub>vci</sub>	-	2.5	8	mA	

#### 4.2 Backlight Unit

The back-light system is an edge-lighting type with six white LED (Light Emitting Diodes)

(Ta=25±2°C)

Item	Symbol	Value			Unit	Condition
		Min.	Typ.	Max.		
LED Voltage	V <sub>F</sub>	-	(19.2)	-	V	
LED Current	I <sub>B</sub>	-	20	-	mA	
Power Consumption	P <sub>LED</sub>	-	(384)	-	mW	
LED Life Time(25°C)	-	(20000)	-	-	hr	

Note (1) 6 LEDs serial type.

(2) Where I<sub>f</sub> = 20mA, P<sub>LED</sub> = V<sub>F</sub> × I<sub>B</sub>

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## 5 Touch Screen Panel Specifications

### 5.1 Touch Screen Panel Specifications

#### 5.1.1 Electrical Characteristics

Item	Min.	Typ.	Max.	Unit	Note
Linearity	-1.5	-	1.5	%	Analog X and Y directions
Terminal resistance	200	-	900	$\Omega$	X (Glass side)
	200	-	900	$\Omega$	Y (Film side)
Insulation resistance	20	-	-	M $\Omega$	DC 25V
Voltage	3.0	-	15.0	V	DC
Chattering	-	-	10	ms	100k $\Omega$ pull-up

Caution (1) : Do not operate it with a thing except a polyacetal pen (tip R0.8mm or less) or a finger, especially those with hard or sharp tips such as a ball point pen or a mechanical pencil.

#### 5.1.2 Mechanical & Reliability Characteristics

Item	Min.	Typ.	Max.	Unit	Note
Activation force	-		80	g	(1)
Durability-surface scratching	Write 100,000	-	-	characters	(2)
Durability-surface pitting	1,000,000	-	-	touches	(3)
Surface hardness	3	-	-	H	JIS K5400,ASTM D3363

Note (1) Stylus pen Input : R0.8mm polyacetal pen or Finger

Note (2) Measurement for Surface area - Scratch 100,000 times straight line on the Film with a stylus change every 20,000times

- Force : 150gf
- Speed : 100mm/sec
- Stylus : R0.8 polyacetal tip

Note (2) Pit 1,000,000 times on the Film with a R8.0 silicon rubber.

- Force : Force : 250gf
- Speed : 3times/sec

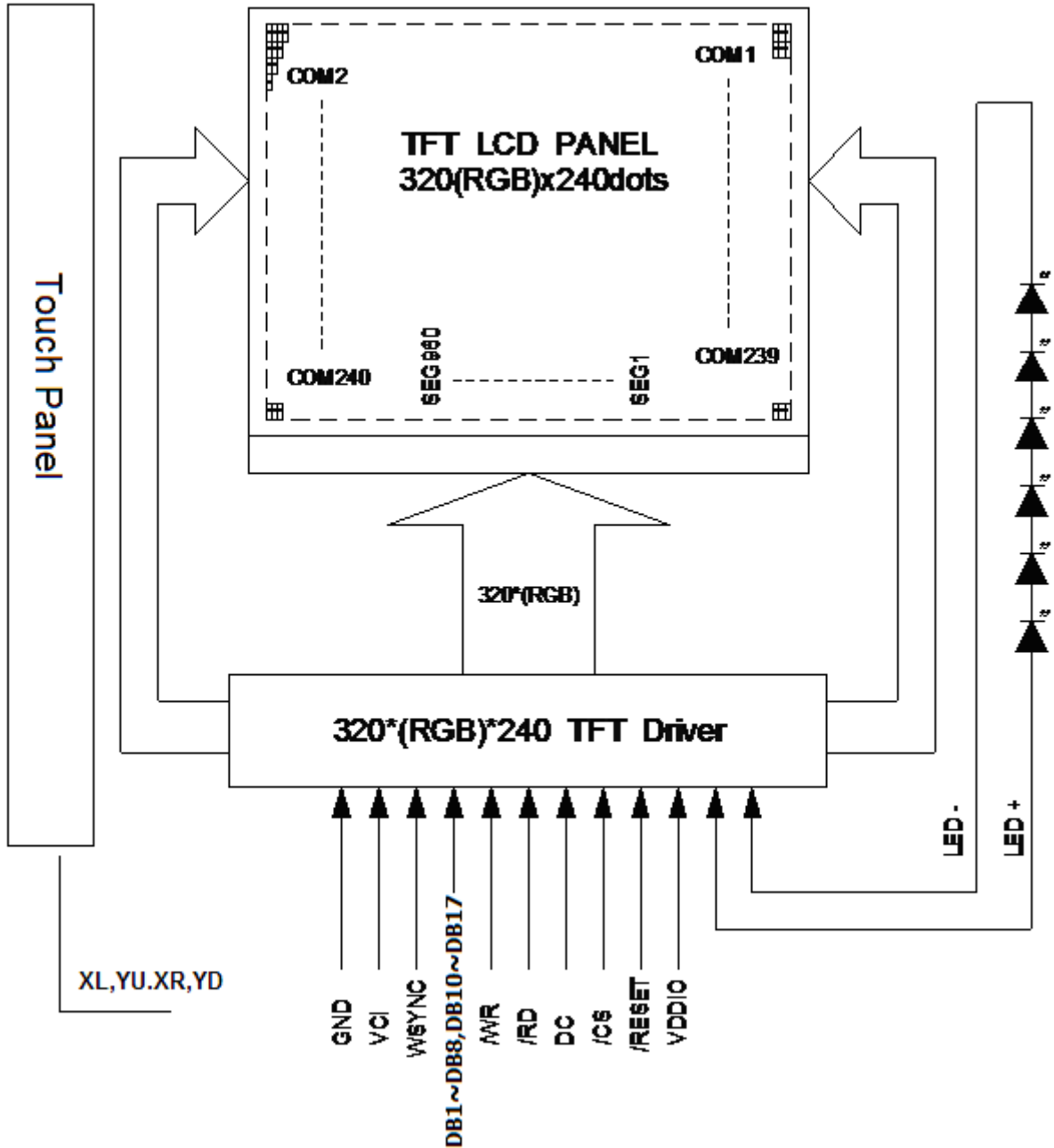


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### 6 Block Diagram

#### 6.1 Interface System Structure with Back Light Unit



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### 7 Input Terminal Pin Assignment

#### 7.1 Pin Assignment

Pin No.	Symbol	I/O	Function
1	LED_A	P	Backlight LED Power
2	LED_K	P	Backlight LED Ground
3	V <sub>DDIO</sub>	P	Logic power supply(+3.0~3.6V)
4	NC	I/O	Unused data pins should connect to GND.
5~12	DB1~DB8	I/O	Data bus
13	NC	I/O	Unused data pins should connect to GND.
14~21	DB10~DB17	I/O	Data bus
22	WSYNC	O	Ram Write Synchronization output. Leave it open when not used.
23	GND	P	Ground
24	/CS	I	Chip select pin for 6800/8080 Parallel Interface. Low: chip can be accessed; High: chip cannot be accessed.
25	RW/WR	I	6800:Indicates read cycle when High, write cycle when Low. 8080: Write signal.
26	/RD	I	6800: Enable signal, 8080: Read signal
27	DC	I	Data or Command.
28	/RESET	I	Reset.
29	V <sub>DDIO</sub>	P	Logic power supply(+3.0~3.6V)
30	VCI	P	Booster input voltage pin. - Connect to voltage source between 2.5V to 3.6V
31	XL	I	Touch Panel Left Side.
32	YD	I	Touch Panel Bottom Side.
33	XR	I	Touch Panel Right Side.
34	YU	I	Touch Panel Top Side.
35	GND	P	Ground
36	GND	P	Ground

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### 8 Optical Characteristics

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (a).  
 Measuring equipment: BM-5A , BM-7

(Ta=25±2°C)

Item	Symbol	Condition	Min	Type	Max	Unit	Note	
Brightness	--	--	(200)	250	--	cd/m <sup>2</sup>	(a)	
Response time	T <sub>R</sub>	θ=0°	--	(8)	(12)	ms	(a),(b)	
	T <sub>F</sub>		--	(17)	(23)	ms		
Contrast ratio	CR	At optimized viewing angle	(320)	(400)	--	--	(c)	
Color Chromaticity	Red	R <sub>X</sub>	θ=0° Normal Viewing Angle	(0.597)	(0.647)	(0.697)	--	(h)
		R <sub>Y</sub>		(0.286)	(0.336)	(0.386)		
	Green	G <sub>X</sub>		(0.26)	(0.31)	(0.36)	--	
		G <sub>Y</sub>		(0.526)	(0.576)	(0.626)		
	Blue	B <sub>X</sub>		(0.086)	(0.136)	(0.186)	--	
		B <sub>Y</sub>		(0.079)	(0.129)	(0.179)		
	White	W <sub>X</sub>		(0.257)	(0.307)	(0.357)	--	
		W <sub>Y</sub>		(0.305)	(0.355)	(0.405)		
Viewing Angle (6H) Gary inversion	Hor.	θ <sub>R</sub>	CR≥10	50	(60)	--	Degree	(e)
		θ <sub>L</sub>		50	(60)	--		
	Ver.	φ <sub>H</sub>		40	(50)	--		
		φ <sub>L</sub>		50	(60)	--		

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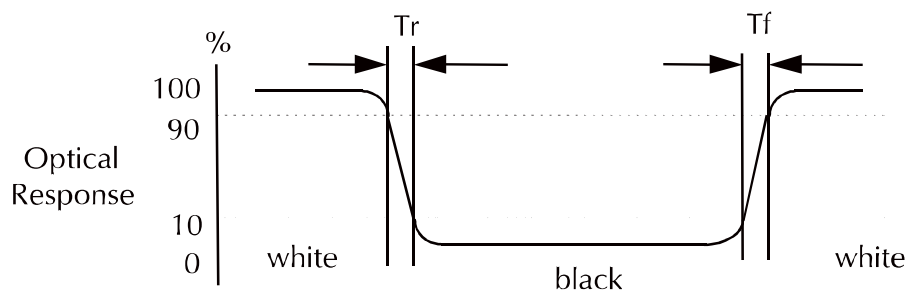
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a. Test equipment setup

After stabilizing and leaving the panel alone shall be warmed up for the stable operation of LCM, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7(fast) with a viewing angle of 2° at a distance of 50cm and normal direction.

b. Definition of response time: Tr and Tf

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



c. Definition of contrast ratio:

Brightness measured when LCD is at "white state"

$$\text{Contrast Ratio (CR)} = \frac{\text{Brightness measured when LCD is at "white state"}}{\text{Brightness measured when LCD is at "black state"}}$$

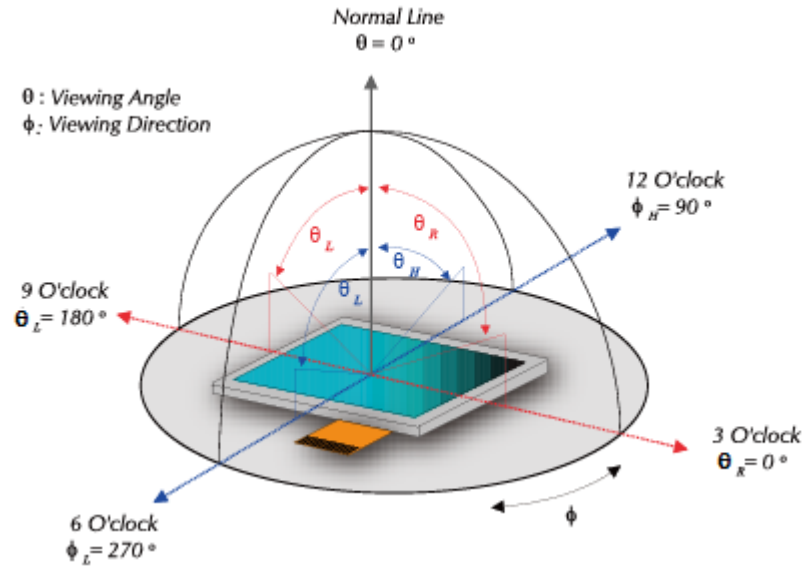
Brightness measured when LCD is at "black state"

d. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

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e. View Angle



f. Definition of Luminance of White: Luminance of white at the center points

Light Source of Back-Light Unit	LED Type
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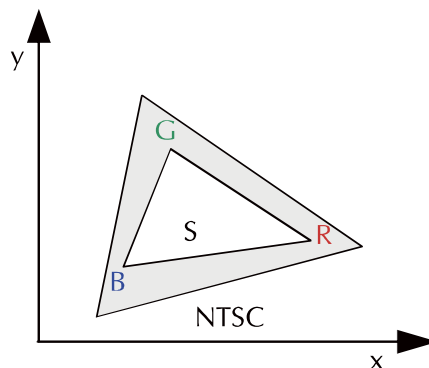
g. Definition of White Uniformity

$$\text{White Uniformity} = \frac{\text{Min. luminance of white among 9-points}}{\text{Max. luminance of white among 9-points}}$$

h. The definition of Color Gamut -Color Chromaticity CIE 1931

Color coordinate of white & red, green, blue at center point.

$$\text{Color Gamut : NTSC(\%)} = \left( \frac{\text{RGB Triangle Area}}{\text{NTSC Triangle Area}} \right) \times 100$$



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## 9 Basic Display Color and Gray Scale

### 9.1 Mapping for writing an Instruction

		Hardware pins																	
Interface	Cycle	D17	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
18 bits		IB15	IB14	IB13	IB12	IB11	IB10	IB9	IB8	x	IB7	IB6	IB5	IB4	IB3	IB2	IB1	IB0	x
16 bits		IB15	IB14	IB13	IB12	IB11	IB10	IB9	IB8		IB7	IB6	IB5	IB4	IB3	IB2	IB1	IB0	
9 bits	1 <sup>st</sup>	IB15	IB14	IB13	IB12	IB11	IB10	IB9	IB8	x									
	2 <sup>nd</sup>	IB7	IB6	IB5	IB4	IB3	IB2	IB1	IB0	x									
8 bits	1 <sup>st</sup>	IB15	IB14	IB13	IB12	IB11	IB10	IB9	IB8										
	2 <sup>nd</sup>	IB7	IB6	IB5	IB4	IB3	IB2	IB1	IB0										

Remark :     x            Don't care bits  
    Not connected pins

### 9.2 Mapping for writing an Pixel Data

			Hardware pins																	
Interface	Color mode	Cycle	D17	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
18 bits	262k		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
16 bits	262k	1 <sup>st</sup>	R5	R4	R3	R2	R1	R0	x	x		G5	G4	G3	G2	G1	G0	x	x	
		2 <sup>nd</sup>	B5	B4	B3	B2	B1	B0	x	x		R5	R4	R3	R2	R1	R0	x	x	
		3 <sup>rd</sup>	G5	G4	G3	G2	G1	G0	x	x		B5	B4	B3	B2	B1	B0	x	x	
		1 <sup>st</sup>	R5	R4	R3	R2	R1	R0	x	x		G5	G4	G3	G2	G1	G0	x	x	
		2 <sup>nd</sup>	x	x	x	x	x	x	x	x		B5	B4	B3	B2	B1	B0	x	x	
		1 <sup>st</sup>	R5	R4	R3	R2	R1	R0	x	x		G5	G4	G3	G2	G1	G0	x	x	
9 bits	262k	1 <sup>st</sup>	R5	R4	R3	R2	R1	R0	G5	G4	G3									
		2 <sup>nd</sup>	G2	G1	G0	B5	B4	B3	B2	B1	B0									
8 bits	262k	1 <sup>st</sup>	R5	R4	R3	R2	R1	R0	x	x										
		2 <sup>nd</sup>	G5	G4	G3	G2	G1	G0	x	x										
	3 <sup>rd</sup>	B5	B4	B3	B2	B1	B0	x	x											
	65k	1 <sup>st</sup>	R4	R3	R2	R1	R0	G5	G4	G3										
2 <sup>nd</sup>		G2	G1	G0	B4	B3	B2	B1	B0											

Remark :     x            Don't care bits  
    Not connected pins

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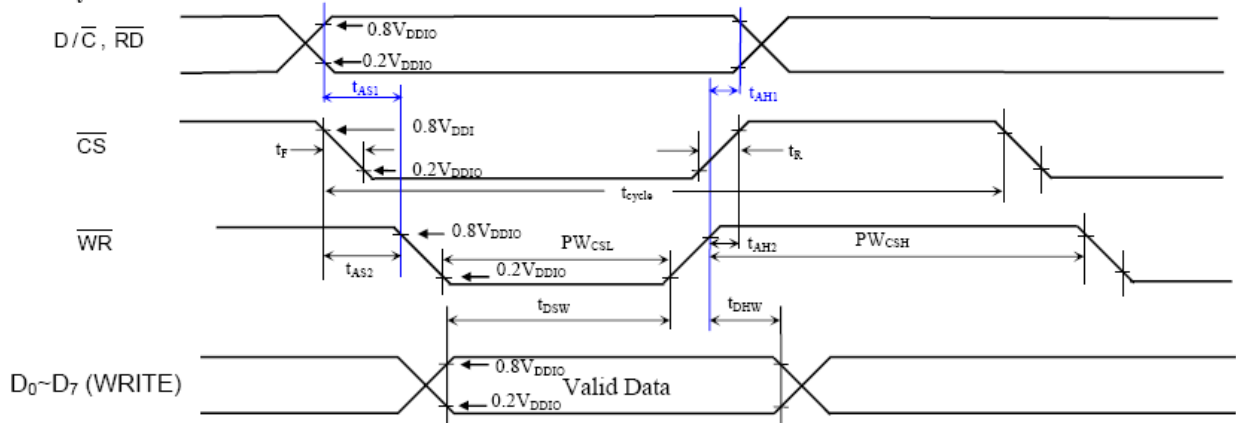
## 10 AC CHARACTERISTICS

### 10.1 Parallel 8080 Timing Characteristics

(Ta=25 ±2°C , V<sub>DDIO</sub> =3.3V)

Item	Symbol	Min.	Typ.	Max.	Unit
Clock Cycle Time (write cycle)	t <sub>cycle</sub>	75	-	-	n <sub>Se</sub>
Clock Cycle Time (read cycle)	t <sub>cycle</sub>	450	-	-	n <sub>Se</sub>
Address Setup Time (R/W)	t <sub>AS</sub>	0	-	-	n <sub>Sec</sub>
Address Hold Time (R/W)	t <sub>AH</sub>	0	-	-	n <sub>Se</sub>
Data Setup Time (D0~D7,WRITE)	t <sub>DSW</sub>	5	-	-	n <sub>Se</sub>
Data Hold Time (D0~D7,WRITE)	t <sub>DHW</sub>	5	-	-	n <sub>Se</sub>
Data Access Time (D0~D7,READ)	t <sub>ACC</sub>	250	-	-	n <sub>Se</sub>
Output Hold time (D0~D7,READ)	t <sub>OH</sub>	100	-	-	n <sub>Se</sub>
Pulse width /CS low (write cycle)	RW <sub>CSL</sub>	40	-	-	n <sub>Sec</sub>
Pulse width /CS high (write cycle)	RW <sub>CSH</sub>	25	-	-	n <sub>Sec</sub>
Pulse width /CS low (write cycle)	RW <sub>CSL</sub>	500	-	-	n <sub>Sec</sub>
Pulse width /CS high (write cycle)	RW <sub>CSH</sub>	500	-	-	n <sub>Sec</sub>
Rise time (/CS)	t <sub>R</sub>	-	-	4	n <sub>Se</sub>
Fall time (/CS)	t <sub>F</sub>	-	-	4	n <sub>Sec</sub>

#### Write Cycle

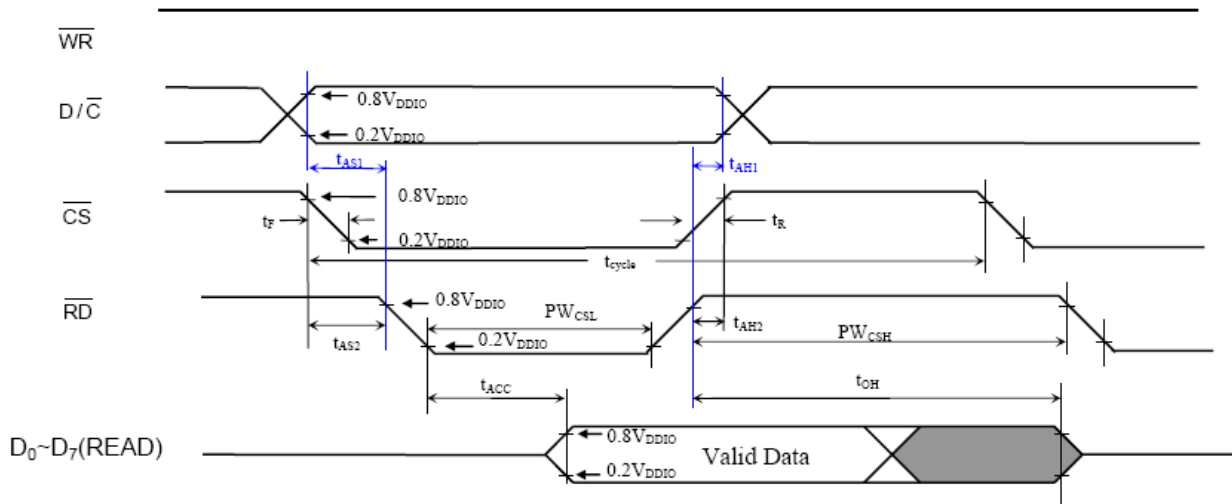


Remark: It's highly recommended that  $\overline{RD}$  remains high for the whole write cycle

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## Read Cycle





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### 11 Reliability

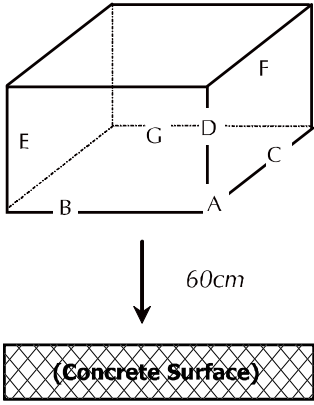
No change on display and in operation under the following test condition.

Condition: Unless otherwise specified, tests will be conducted under the following condition.

Temperature: 20±5°C.

Humidity: 65±5%RH.

Tests will be not conducted under functioning state.

No.	Parameter	Condition	Notes
1	High Temperature Operating	60°C±2°C, 240hrs (Operation state).	
2	Low Temperature Operating	-10°C±2°C, 240hrs (Operation state).	1
3	High Temperature Storage	70°C±2°C, 240hrs.	2
4	Low Temperature Storage	-20°C±2°C, 240hrs.	1,2
5	High Temperature and High Humidity Operation Test	60°C±2°C, 90%, 240hrs	1,2
6	Vibration Test	Total fixed amplitude: 1.5mm. Vibration Frequency: 10~55Hz. One cycle 60 seconds to 3 direction of X, Y, Z each 15 minutes.	3
7.	Drop Test	<p>To be measured after dropping from 60cm high on the concrete surface in packing state.</p> <div style="text-align: center;">  <p style="color: blue; font-style: italic;">Dropping method corner dropping:</p> <p style="color: blue; font-style: italic;">A corner: Once edge dropping.</p> <p style="color: blue; font-style: italic;">B, C, D edge: Once face dropping.</p> <p style="color: blue; font-style: italic;">E, F, G face: Once.</p> </div>	

- Notes:
1. No dew condensation to be observed.
  2. The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.
  3. Vibration test will be conducted to the product itself without putting I in a container.

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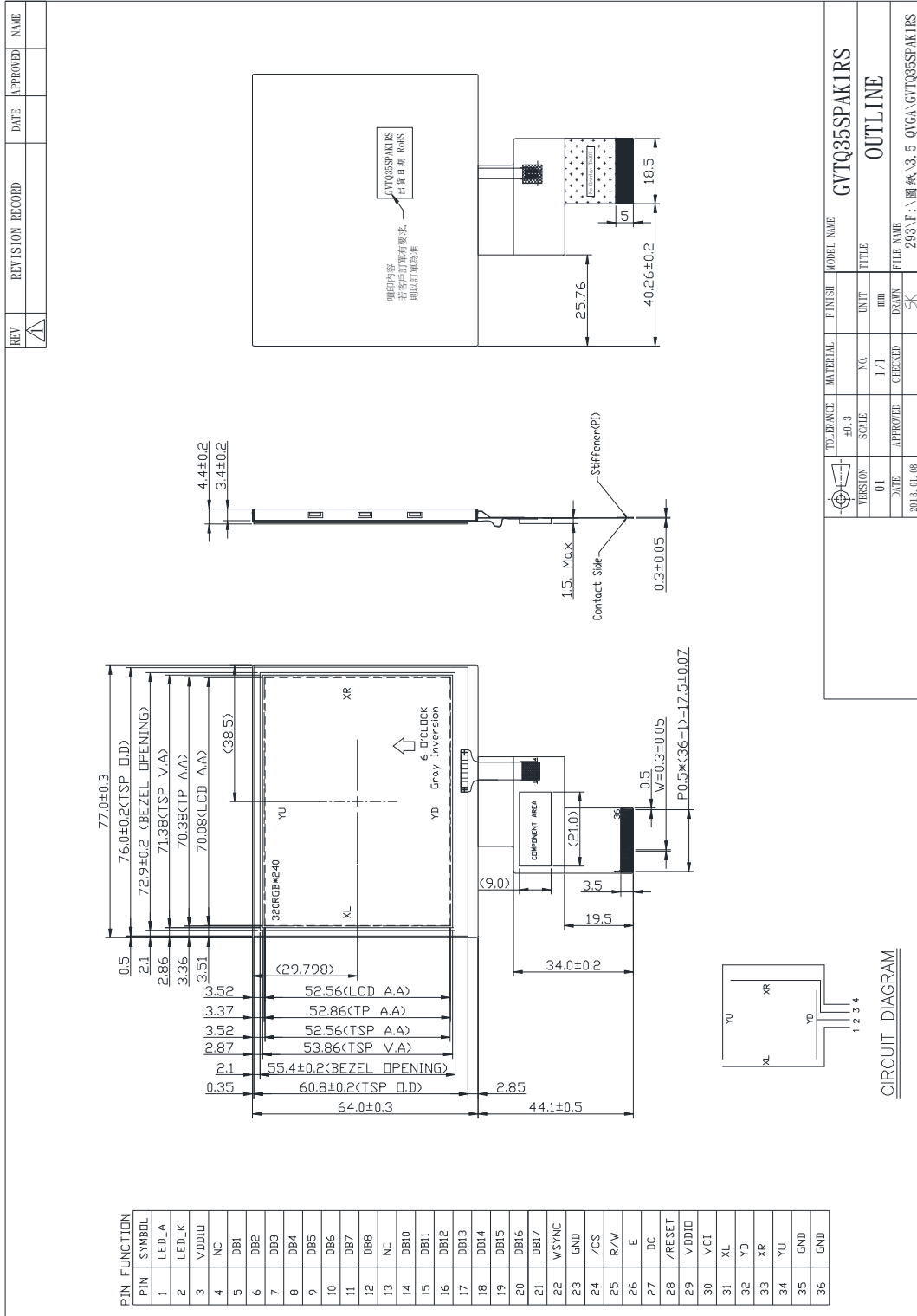
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## 12 Dimensional Outlines



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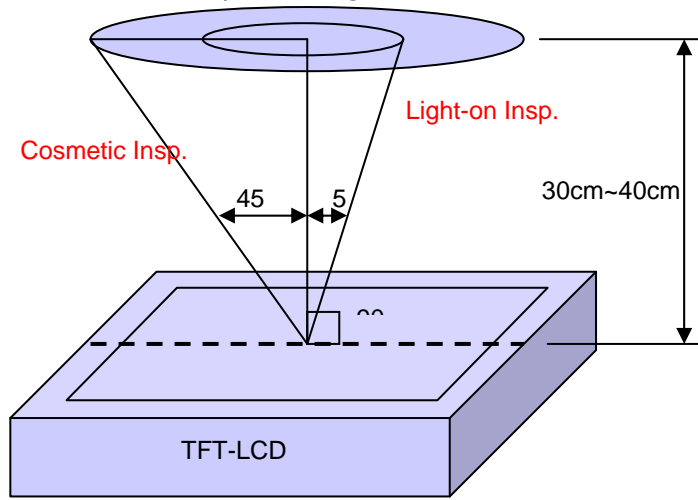
### 13 Incoming Inspection Standards

#### 13.1 Inspection and Environment Conditions

##### 13.1.1 Inspection Conditions:

- (1) Inspection Distance: 35 cm±5cm
- (2) View Angle : Light-on Inspection Angle : ±5°

Cosmetic Inspection Angle : ±45°



##### 13.1.2 Environment Conditions:

Ambient Temperature		23°C ±5°C
Ambient Humidity		55±10%RH
Ambient Illumination	Cosmetic Inspection	more than 600 Lux
	Functional Inspection	300~500 Lux

##### 13.1.3 Sampling Conditions:

- (1) Lot Size: Quantity of shipment lot per model
- (2) Sampling Method:

Sampling Plan		MIL-STD-105E
		Normal Inspection, Single Sampling
		Level II
AQL	Major Defect	1.0%
	Minor Defect	1.5%

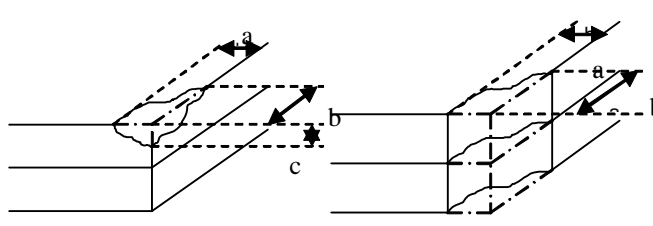

- (3) The classification of Major(MA) and Minor(MI) defects is shown as 3. Inspection Criteria.

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### 13.1.4 Inspection Criteria

#### 13.1.4.1 Cosmetic Inspection(Panel):

Item	Judgment Criteria	Classification
Chipping on Panel	 <p style="text-align: center;"><math>a \leq 3.0\text{mm}</math>, <math>b \leq 3.0\text{mm}</math>, <math>c \leq t</math> ( Bottom glass thickness)</p>	MA
Scratch on Panel *Note-2	$W \leq 0.05\text{mm}$ or $L < 5\text{mm}$ : Ignored $0.05\text{mm} < W \leq 0.1\text{mm}$ and $L \leq 5\text{mm}$ : $N \leq 5$ $W > 0.1\text{mm}$ or $L > 5\text{mm}$ : Not allowed	MI
Bubble or Dent on Panel *Note-3	$D \leq 0.2\text{mm}$ : Ignored $0.2\text{mm} < D \leq 0.3\text{mm}$ : $N \leq 5$ $D > 0.3\text{mm}$ : Not allowed	MI
Panel Crack	 <p style="text-align: center;">Not Allowed crack</p>	MA
Bezel Deformation	Obvious deformation is not allowed.	MI
Bezel Oxidation	Not allowed if it rusts continuously over 1 cm (It is out of warranty with rusted tin plate)	MI
Bezel Scratch	$L \leq 20\text{mm}$ , $W \leq 0.2$ , $N \leq 3$	MI
Metal Squash Dent /Flange(Front Side)	$D(W) \leq 1, L \leq 3, N \leq 3;$	MI
B/L High Voltage Wire Denudation	Not allowed	MA
Polarizer flaw or leak out resin	Defect is defined as the active area.	MI
Outline Dimension	Must in Spec, refer to related product spec.	MI

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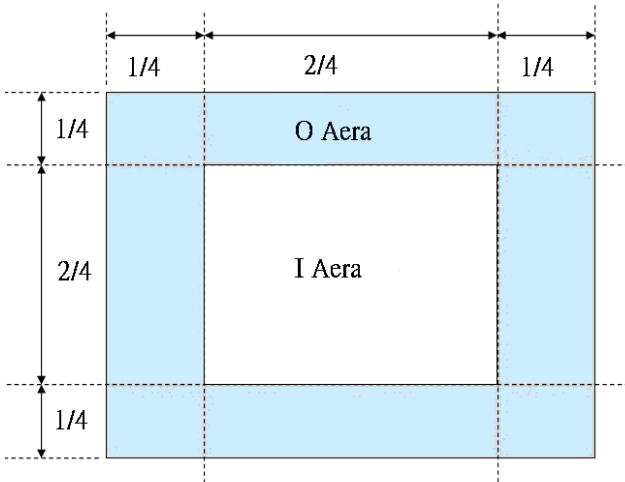
13.1.4.2 Functional Inspection:

Item	Judgment Criteria			Classification
	Area(Note1)	I	O	
Point Defect	Bright dot	Random	2	
		2 dots adjacent	0	0
		3 dots adjacent or more	0	0
	Dark dot	Random	3	
		2 dots adjacent	0	
		3 dots adjacent or more	0	0
	Total Dot Defect		5	
	Distance	Distance between Bright and Bright dot	$L \geq 5\text{mm}$	
		Distance between Bright and Dark dot	$L \geq 5\text{mm}$	
		Distance between Dark dot	$L \geq 5\text{mm}$	
(1) It is defined as Point Defect if defect area > 0.5dot (2) It is ignored if defect area $\leq 0.5\text{dot}$ (3) Weak point defect will be defined as Bright Dot if it can be observed through ND filter 5%( Full Screen Black Inspection)				
Line Defect	Obvious vertical or horizontal line defect is not allowed.			MA
Mura	Not allowed if it can be observed through ND Filter 5 %			MI
Foreign Material in spot shape *Note-3	$D \leq 0.2\text{mm}$ : Ignored $0.2\text{mm} < D \leq 0.5\text{mm}$ : $N \leq 8$ $D > 0.5\text{mm}$ : Not allowed			MI
Foreign Material in line or spiral shape *Note-4	$W \leq 0.05\text{mm}$ or $L \leq 5\text{mm}$ : Ignored $0.05\text{mm} < W \leq 0.2\text{mm}$ and $L 1.0\text{mm} \leq 5\text{mm}$ : $N \leq 8$ $W > 0.2\text{mm}$ or $L > 5\text{mm}$ : Not allowed			MI
Display Function Abnormal	No Malfunction can be allowed			MA

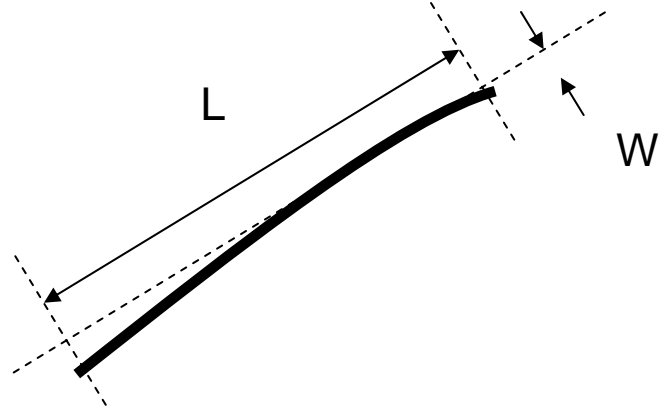
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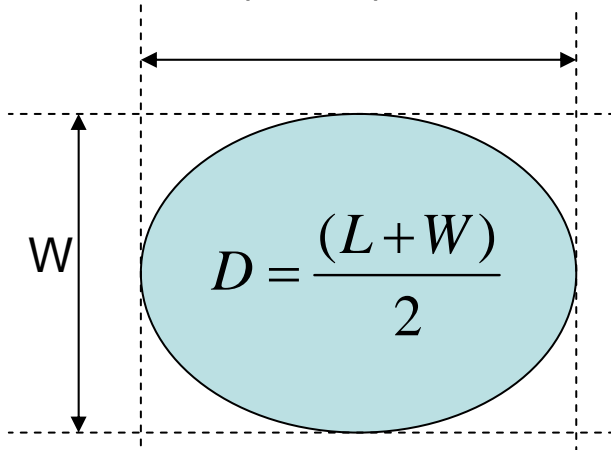
Note-1 : I/O Area Definition



Note-2 : Polarizer Scratch



Note-3 : Spot Foreign Material  
( $W \geq L / 4$ )



Note-4 : Line or Spiral Foreign Material  
( $W < L / 4$ )

